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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/665,474

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Seok Woo Lee

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MCKENNA LONG & ALDRIDGE LLP
1900 K STREET, NW
WASHINGTON, DC 20006

EXAMINER

CALEY, MICHAEL H

ART UNIT

PAPER NUMBER

2871

MAIL DATE

DELIVERY MODE

03/17/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/665,474	Applicant(s) LEE ET AL.	
	Examiner MICHAEL H. CALEY	Art Unit 2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 December 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-6 and 8-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 6 and 9-17 is/are rejected.
- 7) ☒ Claim(s) 5 and 8 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Nakatani et al. (U.S. Patent No. 4,862,153 “Nakatani”).

Regarding claim 1, Nakatani discloses a LCD device comprising:

- a main support (Figure 5 element 4);
- a liquid crystal panel (Figure 5 element 6) installed on a front surface of the main support;
- a panel guide (Figure 5 element 3) between the main support and the liquid crystal panel to support the liquid crystal panel; and
- a film (Figure 5 element 2) connected to the liquid crystal panel with a drive integrated circuit (Figure 5 element 1; Column 4 lines 43-44) that drives the liquid crystal panel mounted thereon, wherein the drive integrated circuit is disposed apart from the front surface of the panel guide (Figure 5).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 2-4, 6, 9, and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakatani in view of Murayama et al. (U.S. Patent No. 6,160,605 “Murayama”).

Regarding claim 2, Nakatani fails to disclose a control board electrically connected to the film. Murayama, however, teaches a control board (Figure 3 element 109) as electrically connected to an analogous film (Figure 3 elements 207 and 210).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to have connected a control board to the external connection element disclosed by Nakatani such that the control board would be electrically connected to the film. Murayama teaches such a control board as essential to provide the power supply voltage and control signals to the drive integrated circuits (Column 6 lines 11-18). One would have been motivated to provide such a control board in the display device disclosed by Nakatani as a means of providing power and control signals to the driving IC devices, thus enabling a functioning display.

Regarding claim 3, Nakatani as modified by Murayama discloses a printed circuit board between the film and the control board and a plurality of signal wiring patterns for transmitting a

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control signal from the control board to the film (Nakatani: Figure 5 element 7; Column 4 lines 8-9).

Regarding claims 4 and 9, Nakatani as modified by Murayama discloses a flexible printed circuit film connecting the printed circuit board to the control board (Murayama: Figure 3 element 110).

Regarding claim 6, Nakatani discloses the main support as having a side surface and the printed circuit board as facing only the side surface of the main support (Column 4 lines 29-42).

Regarding claim 10, Nakatani discloses the control board as disposed at a rear surface of the main support (Figure 5 elements 4 and 7).

Claims 11-17 are rejected under 35 U.S.C. 103(a) as being unpatentable Nakatani in view of Ueda et al. (U.S. Patent No. 5,838,412 “Ueda”).

Regarding claims 11-14, Nakatani fails to disclose a metal case top as combined with the main support by screws and as bent to cover an edge of the liquid crystal panel. Ueda, however, teaches such a case top (Figure 26 element SHD; Column 8 lines 11-19; Column 9 lines 11-15).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to have included a case top as proposed in the liquid crystal display device disclosed by Nakatani. One would have been motivated to include such a case top as a means of

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protecting the top side of the display device as well as to aid in securely fixing the display components (Column 9 lines 28-31).

Regarding claim 15, Nakatani fails to disclose a backlight. Ueda teaches a backlight as a means of illuminating the liquid crystal display element (Column 20 lines 6-14).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form a backlight in the display device disclosed by Nakatani. One would have been motivated to use a backlight as a means of illuminating the display to make images visible to a user according to conventional means (Column 20 lines 6-14).

Regarding claims 16 and 17, Nakatani fails to explicitly disclose a data film as attached to a data pad on the liquid crystal panel and a gate film as attached to a gate pad. Ueda teaches pads and films as designated as for gate drivers and data drivers (Figure 29).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to have formed the pad and film portions as gate and data film and pad areas. As is conventional in the art, gate and data drivers are necessary to drive an array of thin film transistor (TFT) switching elements. One would have been motivated to form the gate and data pad and film areas accordingly to benefit from the known advantages of having TFT switching elements, such as a fast switching speed.

Allowable Subject Matter

Claims 5 and 8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

Applicant's arguments filed 12/5/07 have been fully considered but they are not persuasive.

Applicant argues claim 1 as allowable over the prior art due to new limitations "wherein the drive integrated circuit is disposed on the film apart from the front surface of the panel guide". The examiner disagrees with Applicant's arguments and maintains the rejections.

Nakatani discloses the drive integrated circuit (1) on the film (2) as is visible from Figure 5. When the surface of the panel guide (3) supporting the panel (6) is considered to be the "front surface", Figure 5 clearly shows the drive integrated circuit as "apart from the front surface of the panel guide".

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after

the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MICHAEL H. CALEY whose telephone number is (571)272-2286. The examiner can normally be reached on M-F 8:30 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Michael H. Caley/
Primary Examiner, Art Unit 2871